



Tandberg VPC fully Licensed with 8190 encoder/processor card

| License | Status | In Use | Available | Description | Expires | Time Left |
|--------------------|---------|--------|-----------|--|---------|-----------|
| VP/SWO/DOLBY/AC3 | enabled | | 4 | Allows one channel (stereo pair) of audio to be encoded using the Dolby Digital format. Three licences are required for 5.1 encoding. | | |
| VP/SWO/DOLBY/PLUS | enabled | | 4 | Allows a channel pair of audio to be encoded using the Dolby Digital Plus format. | | |
| VP/SWO/DOLBY/E/DEC | enabled | | 4 | Allows for the decode of one Dolby E bitstream consisting of up to 8 channels in transcode. | | |
| VP/SWO/DOLBY/DEC | enabled | | 4 | Allows for the decoding of one Dolby Digital bitstream consisting of up to 5.1 channels in transcode. | | |
| VP/SWO/M1L2 | enabled | | 24 | Allows one stereo pair of audio to be encoded using the MPEG-1 Layer II format | | |
| VP/SWO/SDMP2/420 | enabled | | 4 | Allows the module to code MPEG-2 SD video. | | |
| VP/SWO/SDMP2/NR | enabled | | 4 | This feature will allow temporal and spatial filtering techniques to reduce the presence of noise in the picture before it is encoded | | |
| VP/SWO/HD/MCTF | enabled | | 4 | This feature will allow temporal and spatial filtering techniques to reduce the presence of noise in the picture before it is encoded | | |
| VP/SWO/REFLEX | enabled | | 4 | This will allow the module to be part of a statmux pool of encoders that share their bit rate using an external multiplexer | | |
| VP/SWO/PROFEC | enabled | | 4 | This enables the Pro MPEG FEC feature on the IP output. | | |
| VP/SWO/ACON | enabled | | 4 | This enables Auto Concatenation. | | |
| VP/SWO/HD10bit | enabled | | 4 | Allows 10 bit encoding in HD H264 4:2:2 mode | | |
| VP/SWO/AAC | enabled | | 4 | Allows one channel (stereo pair) of audio to be encoded using the AAC/HE-AAC format. | | |
| VP/SWO/SD/MCTF | enabled | | 4 | This feature will allow temporal and spatial filtering techniques to reduce the presence of noise in the SD picture before it is encoded | | |
| VP/SWO/DPI | enabled | | 4 | This enables the video splicing feature. | | |
| VP/SWO/PAA | enabled | | 4 | This feature enables phase aligned MPEG-2 Layer II audio encode. | | |
| VP/SWO/PIP | enabled | | 4 | This enables one PiP Channel. | | |
| VP/SWO/PIP/PLUS | enabled | | 4 | This enables one SD resolution PiP Channel. | | |
| VP/SWO/DTV | enabled | | 4 | | | |
| VP/SWO/ALC | enabled | | 4 | Enables one stereo pair of Automatic Loudness Control (VP/SWO/ALC). | | |



| | | | |
|------------------------|---------|---|---|
| VP/SWO/SDAVC2 | enabled | 4 | Enables one multistream SD H.264 Video Encoder |
| VP/SWO/SDMP22 | enabled | 4 | Enables one multistream SD MPEG-2 Video Encoder |
| VP/SWO/HDAVC2 | enabled | 4 | Enables one multistream HD H.264 Video Encoder |
| VP/SWO/HDMP22 | enabled | 4 | Enables one multistream HD MPEG-2 Video Encoder |
| VP/SWO/PSIP | enabled | 4 | Enables PSIP carousel support via the RCP interface |
| VP/SWO/ROGERS-8180MP2S | enabled | 4 | ROGERS-8180 MPEG2 SD-only license |

| Time | Model | Name | IP Address | MAC Address | Build | Build Date | Host Software | Host BSP | PSUs | Slot 1 | 2 | 3 | 4 | 5 | 6 | IP Output | | |
|------|------------|------|---------------|-------------|----------------|------------------------------|---------------|---------------|------|--------------|-------|---|---|---|---|-----------|---|---|
| 53ms | Video Proc | VP | 192.168.1.200 | 0020aa70e84 | 7.2.16. Build2 | Tue Dec 12 15:27:27 UTC 2017 | 7.2.16.157176 | 7.2.16.155931 | 1 | ICE-4 (Base) | ICE-4 | N | N | N | N | - | - | - |



| | | | | | | | | | | | | | | | | | | | |
|-----|----|--|--|--|--|--|--|--|--|--|--------|---|---|---|---|--|--|--|--|
| ess | or | | | | | | | | | | (Acc.) | n | n | n | n | | | | |
|-----|----|--|--|--|--|--|--|--|--|--|--------|---|---|---|---|--|--|--|--|

| | Actual Version | Expected Version | |
|--------|----------------|------------------|------------|
| System | 7.2.16.Build2 | | Version OK |

Host

| | Actual Version | Expected Version | |
|---------------------|----------------|------------------|------------|
| Applications | 7.2.16.157176 | 7.2.16.157176 | Version OK |
| BootFPGAFactory | 0.1.7.20227 | | Version OK |
| BootFPGAApplication | 0.1.7.20227 | 0.1.7.20227 | Version OK |
| ClockFPGA | 1.0.6.80882 | 1.0.6.80882 | Version OK |
| MainFPGA | 2.15.8.124854 | 2.15.8.124854 | Version OK |
| HostBSPFactory | 1.1.3.36652 | | Version OK |
| HostBSPApplication | 7.2.16.155931 | 7.2.16.155931 | Version OK |
| Host Package | 7.2.16.157176 | | |

Slot 1

| | Actual Version | Expected Version | |
|--------------------------|------------------|------------------|------------|
| Applications | 7.2.16.155931 | 7.2.16.155931 | Version OK |
| AudioDSP | 1.11.1009.149206 | 1.11.1009.149206 | Version OK |
| Look-ahead_Main_Software | 5.0.0.4374 | 5.0.0.4374 | Version OK |
| Look-ahead_DSP_Software | 5.0.0.4374 | 5.0.0.4374 | Version OK |
| Look-ahead_BLD_Software | 0.0.0.610 | 0.0.0.610 | Version OK |
| BackEndFPGAApplication | 2.0.10.100261 | 2.0.10.100261 | Version OK |
| BackEndFPGAFailSafe | 0.1.11.32619 | | Version OK |
| BSPFailSafe | 1.1.3.36663 | | Version OK |
| PreProcessor | 7.2.19.138612 | 7.2.19.138612 | Version OK |
| BSPApplication | 7.2.16.75618 | 7.2.16.75618 | Version OK |
| Slot 1 Package | 7.2.16.155931 | | |

Slot 2

| | Actual Version | Expected Version | |
|-----------------------------|----------------|------------------|------------|
| Applications | 7.2.16.155931 | 7.2.16.155931 | Version OK |
| MPEG4_VideoDSP | 9.5.3.117124 | 9.5.3.117124 | Version OK |
| MPEG2_VideoDSP | 7.1.3.129715 | 7.1.3.129715 | Version OK |
| MPEG4_Motion_Estimator_FPGA | 0.1.1.37987 | 0.1.1.37987 | Version OK |
| MPEG2_Motion_Estimator_FPGA | 0.1.2.55397 | 0.1.2.55397 | Version OK |
| MPEG4_CodingFPGA | 0.1.1.77196 | 0.1.1.77196 | Version OK |
| MPEG2_CodingFPGA | 0.1.2.96531 | 0.1.2.96531 | Version OK |
| BackEndFPGAApplication | 7.0.9.124794 | 7.0.9.124794 | Version OK |



| | | | |
|---------------------|---------------|--------------|------------|
| BackEndFPGAFailSafe | 0.0.7.32990 | | Version OK |
| BSPFailSafe | 1.1.3.36623 | | Version OK |
| BSPApplication | 7.2.16.75962 | 7.2.16.75962 | Version OK |
| Slot 2 Package | 7.2.16.155931 | | |

Browser: http://192.168.1.200/

Video Processor

ERICSSON

Fri Jun 15 13:27:08 2018

Outputs
IP Output: Data Port 3 - Data Port 4

Navigation: Status | Alarms | **Configure** | Versions | Support

System Configuration: Option Slots | Device Info | **System** | Transcode | Save-Load | Stored Configurations | Licenses

Slots / Slot 1 / EN8190 Pre-Processor / Video Encoders / Video 1 / Encode

Buttons: Back | Up | Refresh | Apply Changes

back panel

Video Profile
Sets the encoder profile. The profile will also affect the allowed bitrate range.

Profile Off
SD H.264 Main Profile Level 3.0 4:2:0 8 bit
SD H.264 High Profile Level 3.0 4:2:0 8 bit
HD MPEG2 Main Profile High Level 4:2:0 8 bit
HD H.264 Main Profile Level 4.0 4:2:0 8 bit
HD H.264 High Profile Level 4.0 4:2:0 8 bit
HD H.264 Hi422 Profile Level 4.1 4:2:2 10 bit

Resolution: 1920 x 1080

Video Bitrate: 3.000 (1.000 Mbit/s - 25.000 Mbit/s)

Buffer Mode: CBR

Buffer Sync Mode: Independent
Selects the Video component with which to match buffer delay, or vary independently.

Aspect Ratio: 16:9

GOP Length: 32 (4 - 250)

GOP Structure: IBBBP
Sets the picture type structure for the GOP

Restricted GOP length: On
Prevents the GOP length from being extended on scene cuts or splice points.

Scene Cut Detection: On
Sets if an I-Frame will be inserted on scene changes, changing GOP length

Delay: 4047 ms

Max CABAC Bit Rate: 50.000 (20.000 Mbit/s - 62.500 Mbit/s)
Controls the bit-rate at which the encoder switches from using CABAC to CAVLC.

MCTE: Off

Result: success

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