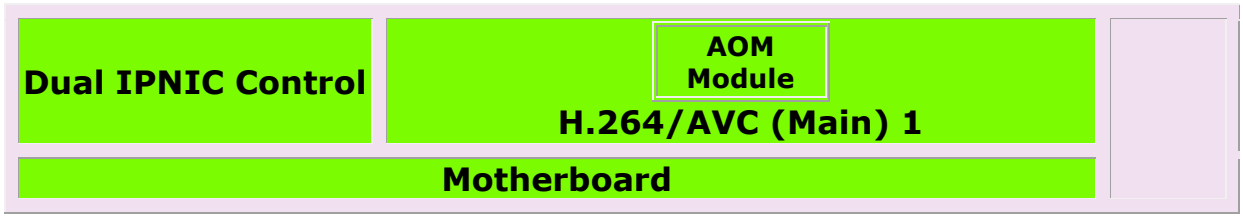
## Tandberg EN8090 ASI, IP, AOM, Unit no. 3483

| Slot | Id | Name | Serial Number | Software | Build Date | Hardware | Firmware |
|---|---|---|---|---|---|---|---|
| 0 | 1100 | Motherboard | 17189 | v4.3.1.60 | Nov 11 2015, 09:51:43 | 7 | v4.4 |
| 1 | 1167 | Dual IPNIC Control | | v4.13 | Unknown | v1 | v3.13 |
| 2 | 1108 | H.264/AVC (Main) 1 | | v3.1.19 | Aug 21 2013, 13:57:14 | v2.1 | v1.0 |

## View of Encoder Rear Panel




| | Code | Description | Licence | H/W | Details |
|---|---|---|---|---|---|
| **Video** | | | | | |
| | M2/ES02/CVBS | Composite Video In | Enabled | OK | |
| | M2/ES02/SDI | SDI Video In | Enabled | OK | |
| | EN8000/SWO/NR | Noise Reduction | Enabled | OK | |
| | M2/ES02/PU | Performance Upgrade | Enabled | OK | |
| | M2/ES02/VBR | Reflex | Enabled | OK | |
| | EN8000/SWO/MPEG2/422 | 422P@ML | Enabled | OK | |
| | M2/ES02/ACON | Auto Concatenation | Enabled | OK | |
| | EN8000/SWO/AVC | H.264/AVC Capable | Enabled | OK | |
| | EN8000/SWO/MPEG2 | Dual AVC and MPEG | Enabled | OK | |
| | EN8000/SWO/HD | H.264/AVC HD (EN8090) | Enabled | OK | |
| | EN8000/SWO/PIP | PiP (Picture in Picture - SIF Res) | Enabled | OK | |
| | EN8000/SWO/PIP/PLUS | PiP-Plus (Full Res SD PiP for HD) | Enabled | OK | |
| | EN8000/SWO/REFLEX | WM or H.264/AVC Reflex | Enabled | OK | |
| | EN8000/SWO/DTV | DTV Features | Enabled | OK | |
| | EN8000/SWO/DBF | Input De-blocking filter | Enabled | OK | |

| | | | | | |
|---|---|---|---|---|---|
| | EN8000/SWO/TLG | Tilgin Compatible AVC Video | Enabled | OK | |
| | EN8000/SWO/SDAVC2 | Dual SD AVC Video | Enabled | OK | |
| **Audio A/B** | | | | | |
| | EN8000/SWO/AC3 | Dolby Digital | Enabled | OK | |
| | M2/ES02/DTS | DTS Audio | Enabled | OK | |
| | M2/ES02/AUDIO2 | Audio:2 Stereo Pairs | Enabled | OK | |
| **Advanced Audio 2A/2B** | | | | | |
| | EN8000/SWO/ICE3AAC | ICE-3 AAC Audio in SD | Enabled | OK | |
| **Advanced Audio 3A-3D** | | | | | |
| | EN8000/SWO/AOMAAC | AAC Audio | Enabled | OK | 4 Stereo Pairs |
| | EN8000/SWO/MPEG4/HEAAC | MPEG-4 HE-AAC Audio | Enabled | OK | 4 Stereo Pairs |
| | EN8000/SWO/MPEG4/HEAACV2 | MPEG-4 HE-AAC v2 Audio | Enabled | OK | 4 Stereo Pairs |
| | EN8000/SWO/D_ENCODE | Encode Dolby Digital (AVC) | Enabled | OK | 4 Stereo Pairs |
| | EN8000/SWO/M1L2/SRND | MPEG 1 Layer II Aligned | Enabled | OK | |
| | EN8000/SWO/DTS | DTS Encode | Enabled | OK | |
| | EN8000/SWO/DOLBY_E/DECODE | Dolby E | Enabled | OK | |
| **VBI data** | | | | | |
| | M2/ES02/DATA | Main Data Input | Enabled | OK | |
| | M2/ES02/VBI | Advanced VBI | Enabled | OK | |
| | EN8000/SWO/HDVBI | MPEG-4 HD VBI | Enabled | OK | |
| **Option Modules** | | | | | |
| | M2/ES02/ATM | ATM | Enabled | H/W Not Present | |
| | M2/ES02/RM | Remux | Enabled | H/W Not Present | |
| | EN8000/SWO/PROFEC | Pro MPEG FEC | Enabled | OK | |
| **Other** | | | | | |
| | EN8000/SWO/BISS | BISS | Enabled | H/W Not Present | |

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|---|---|---|---|---|
| | **M2/ES02/PS** | **PSIP Insertion** | **Enabled** | **H/W Not Present** |
| | **EN8000/SWO/RAS** | **RAS** | **Enabled** | **OK** |
| | **EN8000/SWO/DPI** | **DPI/SCTE-35 Splice Points** | **Enabled** | **OK** |
| | EN8000/SWO/AUTH | HTTP Authentication | Disabled | OK |

## Build Version 3.1.30

| Id | Version | Expected | Actual | Status |
|---|---|---|---|---|
| **General** | | | | |
| 2 | Software Release | v4.3.1.60 | v4.3.1.60 | OK |
| 39 | Motherboard Mod Level | Any | 3 | OK |
| 38 | PCB Issue | Any | 7 | OK |
| | PLD Version | Any | v4.4 | OK |
| 1 | Boot Software Release | v2.0 | v2.0 | OK |
| | CA Code Version | Any | v4 | OK |
| 3 | Web Page Version | v140 | v140 | OK |
| | PCB Number | Any | S11171 | OK |
| | Backplane PCB Number | Any | S11638 | OK |
| 41 | Backplane Mod Level | Any | v1 | OK |
| 40 | Backplane PLD Version | Any | v1.1 | OK |
| **Mux** | | | | |
| 4 | FPGA Firmware | v40.59 | v40.59 | OK |
| **SD Video Source** | | | | |
| | Software Release | Any | v1.0 | OK |
| **Data B - RS422** | | | | |
| | Version | Any | v2 | OK |
| **Data A - RS232** | | | | |
| | Software Release | Any | v1.0 | OK |
| **Audio A** | | | | |
| | Hardware Release | Any | v8 | OK |
| | Software Release | Any | v3.11 | OK |
| | Firmware Release | Any | v0.1 | OK |
| **MPEG-2 Encoder** | | | | |

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|---|---|---|---|---|
| | PCB Issue.Mod Status | Any | v7.3 | OK |
| 6 | Software Release | v6.13 | v6.13 | OK |
| 7 | FPGA Release | v0.6 | v0.6 | OK |
| | PLD Version | Any | v3 | OK |
| | PCB S Number | Any | S11171 | OK |

**Dual IPNIC Control**

| | | | | |
|---|---|---|---|---|
| 27 | Software Version | v4.13 | v4.13 | OK |
| 28 | Firmware Version | v3.13 | v3.13 | OK |
| 44 | Hardware Version | Any | v1.1 | OK |

**ICE-3 Status**

| | | | | |
|---|---|---|---|---|
| 45 | H/W Version | Any | v2.1 | OK |
| 47 | S/W Version | v3.1.19 | v3.1.19 | OK |
| 57 | CPLD Version | Any | v1.0 | OK |
| 50 | Pre-Processor FPGA | Any | v3.26 | OK |
| 52 | Output FPGA | v16.177 | v16.177 | OK |
| 46 | Boot Code | Any | v0.2 | OK |
| 51 | PreProc S/W Version | v3.36 | v3.36 | OK |
| 48 | Bitstream PPC Boot Version | Any | v0.1 | OK |
| 49 | Bitstream PPC Main Version | v0.36 | v0.36 | OK |
| 53 | AV S/W Version | v3.0.11 | v3.0.11 | OK |
| 54 | AV DSP Version | v3.0.35 | v3.0.35 | OK |
| 55 | PiP Video S/W Version | Any | v0.0 | OK |
| 56 | PiP Video DSP Version | Any | v0.0 | OK |

**Advanced Audio 3A**

| | | | | |
|---|---|---|---|---|
| 24 | S/W Release | v7.99 | v7.99 | OK |
| 26 | CPLD Release | Any | v1.1 | OK |
| 10 | Codec Pack | v9.9 | v9.9 | OK |