



Ericsson VPC (3 Channel - 1 x EN8190, 2 x EN8130) - S/N: 08628

Model

Model: Video Processor

Build Version: 7.2.20.Build1

Serial Number: 08628

Host

License	Status	In Use	Available	Description	Expires	Time Left
VP/SWO/DOLBY/AC3	enabled		100	Allows one channel (stereo pair) of audio to be encoded using the Dolby Digital format. Three licences are required for 5.1 encoding.		
VP/SWO/DOLBY/PLUS	enabled		100	Allows a channel pair of audio to be encoded using the Dolby Digital Plus format.		
VP/SWO/DOLBY/E/DEC	enabled		100	Allows for the decode of one Dolby E bitstream consisting of up to 8 channels in transcode.		
VP/SWO/DOLBY/DEC	enabled		100	Allows for the decoding of one Dolby Digital bitstream consisting of up to 5.1 channels in transcode.		
VP/SWO/M1L2	enabled		400	Allows one stereo pair of audio to be encoded using the MPEG-1 Layer II format		
VP/SWO/SDMP2/420	enabled		100	Allows the module to code MPEG-2 SD video.		
VP/SWO/SDMP2/NR	enabled		100	This feature will allow temporal and spatial filtering techniques to reduce the presence of noise in the picture before it is encoded		
VP/SWO/HD/MCTF	enabled		100	This feature will allow temporal and spatial filtering techniques to reduce the presence of noise in the picture before it is encoded		
VP/SWO/REFLEX	enabled		100	This will allow the module to be part of a statmux pool of encoders that share their bit rate using an external multiplexer		
VP/SWO/PROFEC	enabled		100	This enables the Pro MPEG FEC feature on the IP output.		
VP/SWO/ACON	enabled		100	This enables Auto Concatenation.		
VP/SWO/HD10bit	enabled		100	Allows 10 bit encoding in HD H264 4:2:2 mode		
VP/SWO/AAC	enabled		100	Allows one channel (stereo pair) of audio to be encoded using the AAC/HE-AAC format.		
VP/SWO/SD/MCTF	enabled		100	This feature will allow temporal and spatial filtering techniques to reduce the presence of noise in the SD picture before it is encoded		
VP/SWO/DPI	enabled		100	This enables the video splicing feature.		
VP/SWO/PAA	enabled		100	This feature enables phase aligned MPEG-2 Layer II audio encode.		
VP/SWO/PIP	enabled		100	This enables one PiP Channel.		
VP/SWO/PIP/PLUS	enabled		100	This enables one SD resolution PiP Channel.		
VP/SWO/DTV	enabled		100			



VP/SWO/ALC	enabled	100	Enables one stereo pair of Automatic Loudness Control (VP/SWO/ALC).
VP/SWO/SDAVC2	enabled	100	Enables one multistream SD H.264 Video Encoder
VP/SWO/SDMP22	enabled	100	Enables one multistream SD MPEG-2 Video Encoder
VP/SWO/HDAVC2	enabled	100	Enables one multistream HD H.264 Video Encoder
VP/SWO/HDMP22	enabled	100	Enables one multistream HD MPEG-2 Video Encoder
VP/SWO/PSIP	enabled	100	Enables PSIP carousel support via the RCP interface
VP/SWO/ROGERS-8180MP2S	enabled	100	ROGERS-8180 MPEG2 SD-only license

System

	Actual Version	Expected Version	
System	7.2.20.Build1		Version OK

Host

	Actual Version	Expected Version	
Applications	7.2.20.174769	7.2.20.174769	Version OK
BootFPGAFactory	0.1.7.20227		Version OK
BootFPGAApplication	0.1.7.20227	0.1.7.20227	Version OK
ClockFPGA	1.0.6.80882	1.0.6.80882	Version OK
MainFPGA	2.15.8.124854	2.15.8.124854	Version OK
HostBSPFactory	1.1.3.36652		Version OK
HostBSPAApplication	7.2.20.174769	7.2.20.174769	Version OK
Host Package	7.2.20.174769		

Slot 1

	Actual Version	Expected Version	
Applications	7.2.20.174769	7.2.20.174769	Version OK
AudioDSP	1.11.1010.172548	1.11.1010.172548	Version OK
Look-ahead_Main_Software	5.0.0.4374	5.0.0.4374	Version OK
Look-ahead_DSP_Software	5.0.0.4374	5.0.0.4374	Version OK
Look-ahead_BLD_Software	0.0.0.610	0.0.0.610	Version OK
BackEndFPGAApplication	2.0.10.100261	2.0.10.100261	Version OK
BackEndFPGAFailSafe	0.1.11.32619		Version OK
BSPFailSafe	1.1.3.36663		Version OK
PreProcessor	7.2.19.138612	7.2.19.138612	Version OK
RX8version	4.3.3.1493	4.3.3.1493	Version OK
BSPAApplication	7.2.20.75618	7.2.20.75618	Version OK
Slot 1 Package	7.2.20.174769		

Slot 2

	Actual Version	Expected Version	
Applications	7.2.20.174769	7.2.20.174769	Version OK
MPEG4_VideoDSP	9.5.4.170440	9.5.4.170440	Version OK
MPEG2_VideoDSP	7.1.4.170442	7.1.4.170442	Version OK
MPEG4_Motion_Estimator_FPGA	0.1.1.37987	0.1.1.37987	Version OK
MPEG2_Motion_Estimator_FPGA	0.1.2.55397	0.1.2.55397	Version OK
MPEG4_CodingFPGA	0.1.1.77196	0.1.1.77196	Version OK



MPEG2_CodingFPGA	0.1.2.96531	0.1.2.96531	Version OK
BackEndFPGAApplication	7.0.9.124794	7.0.9.124794	Version OK
BackEndFPGAFailSafe	0.0.7.32990		Version OK
BSPFailSafe	1.1.3.36623		Version OK
BSPApplication	7.2.20.75962	7.2.20.75962	Version OK
Slot 2 Package	7.2.20.174769		

Slot 3

	Actual Version	Expected Version	
Applications	7.2.20.174769	7.2.20.174769	Version OK
AudioDSP	1.11.1010.172548	1.11.1010.172548	Version OK
Look-ahead_Main_Software	5.0.0.4374	5.0.0.4374	Version OK
Look-ahead_DSP_Software	5.0.0.4374	5.0.0.4374	Version OK
Look-ahead_BLD_Software	0.0.0.610	0.0.0.610	Version OK
BackEndFPGAApplication	2.0.10.100261	2.0.10.100261	Version OK
BackEndFPGAFailSafe	0.1.11.32619		Version OK
BSPFailSafe	1.1.3.36663		Version OK
PreProcessor	7.2.19.138612	7.2.19.138612	Version OK
RX8version	4.3.3.1493	4.3.3.1493	Version OK
BSPApplication	7.2.20.75618	7.2.20.75618	Version OK
Slot 3 Package	7.2.20.174769		

Slot 4

	Actual Version	Expected Version	
Applications	7.2.20.174769	7.2.20.174769	Version OK
AudioDSP	1.11.1010.172548	1.11.1010.172548	Version OK
Look-ahead_Main_Software	5.0.0.4374	5.0.0.4374	Version OK
Look-ahead_DSP_Software	5.0.0.4374	5.0.0.4374	Version OK
Look-ahead_BLD_Software	0.0.0.610	0.0.0.610	Version OK
BackEndFPGAApplication	2.0.10.100261	2.0.10.100261	Version OK
BackEndFPGAFailSafe	0.1.11.32619		Version OK
BSPFailSafe	1.1.3.36663		Version OK
PreProcessor	7.2.19.138612	7.2.19.138612	Version OK
RX8version	4.3.3.1493	4.3.3.1493	Version OK
BSPApplication	7.2.20.75618	7.2.20.75618	Version OK
Slot 4 Package	7.2.20.174769		